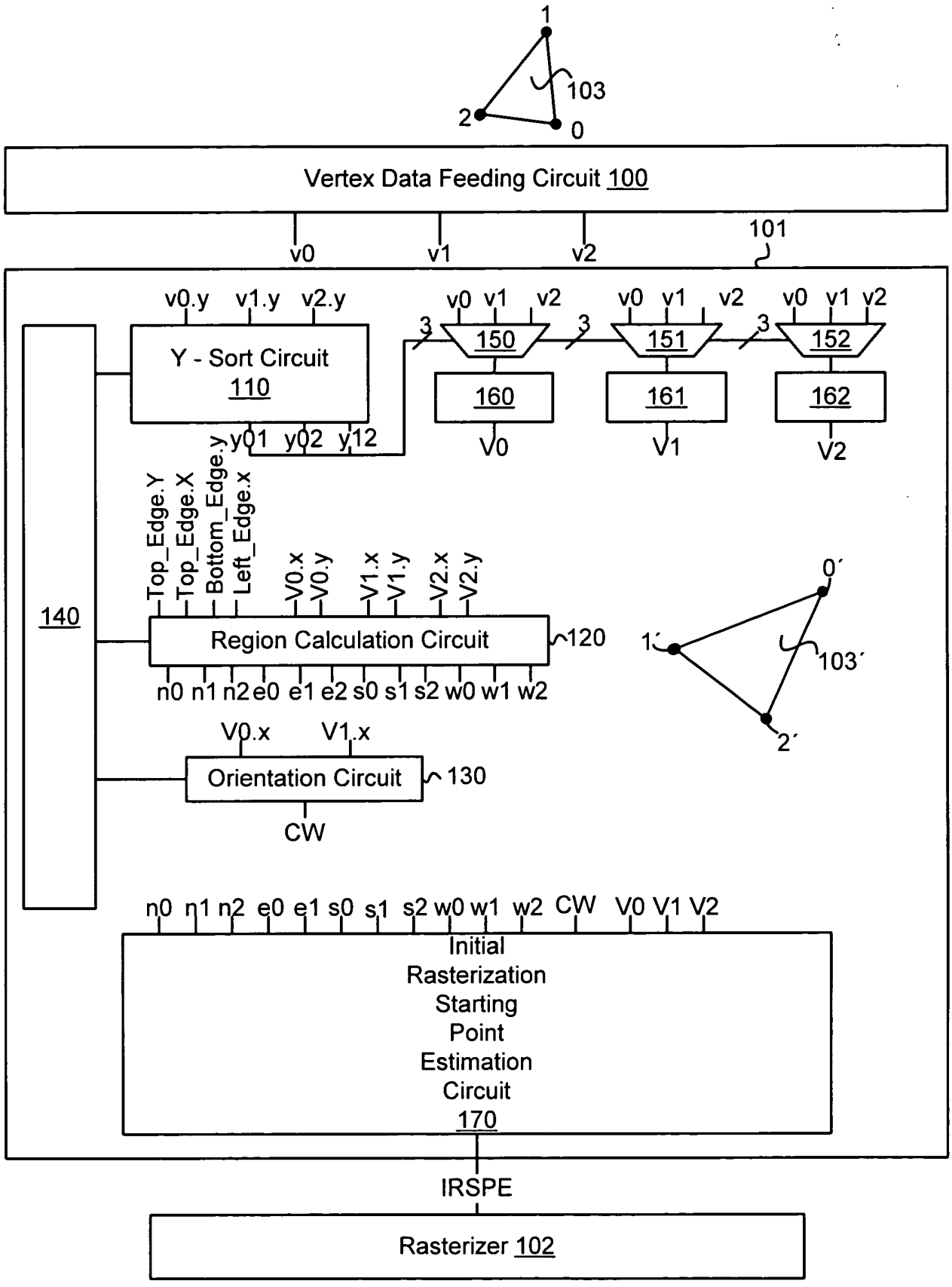
1
103
2
0
Vertex Data Feeding Circuit 100
v0
v1
v2
101
v0.y v1.y v2.y
Y - Sort Circuit
110
y01 y02 y12
v0 v1 v2
3
150
160
V0
151
161
V1
152
162
V2
Top_Edge.Y
Top_Edge.X
Bottom_Edge.y
Left_Edge.x
V0.x
V0.y
V1.x
V1.y
V2.x
V2.y
140
Region Calculation Circuit
120
n0 n1 n2 e0 e1 e2 s0 s1 s2 w0 w1 w2
0'
1'
103'
2'
V0.x
V1.x
Orientation Circuit
130
CW
n0 n1 n2 e0 e1 s0 s1 s2 w0 w1 w2 CW V0 V1 V2
Initial
Rasterization
Starting
Point
Estimation
Circuit
170
IRSPE
Rasterizer 102

FIG. 1